

Fall 2024 ECE Class Schedule

Subj	CRS	Course Title	Cmp	Sctn	Credits	Days	Start Time	End Time	Building	Room	Instr
ESE	118	Digital Logic Design	LEC	1		MW	2:00 PM	3:20 PM	MELVILLE LBR	W4530	Dmitri Donetski
ESE	118	Digital Logic Design	LAB	L01	4	M	3:30 PM	6:20 PM	HVY ENGR LAB	235	Dmitri Donetski
ESE	118	Digital Logic Design	LAB	L03	4	T	3:30 PM	6:20 PM	HVY ENGR LAB	235	Dmitri Donetski
ESE	122	Discrete Math for Engineers	LEC	1	3	MW	6:30 PM	7:50 PM	FREY HALL	100	Wendy Tang
ESE	123	Intro to Elec and Compu Engrng	LEC	1		MW	5:00 PM	6:20 PM	FREY HALL	100	David Westerfeld
ESE	123	Intro to Elec and Compu Engrng	LAB	L01	4	R	3:30 PM	6:20 PM	LGT ENGR LAB	283	David Westerfeld
ESE	123	Intro to Elec and Compu Engrng	LAB	L02	4	T	3:30 PM	6:20 PM	LGT ENGR LAB	283	David Westerfeld
ESE	123	Intro to Elec and Compu Engrng	LAB	L03	4	W	6:30 PM	9:20 PM	LGT ENGR LAB	283	David Westerfeld
ESE	123	Intro to Elec and Compu Engrng	LAB	L04	4	R	6:30 PM	9:20 PM	LGT ENGR LAB	283	David Westerfeld
ESE	123	Intro to Elec and Compu Engrng	LAB	L05	4	R	9:30 AM	12:20 PM	LGT ENGR LAB	283	David Westerfeld
ESE	123	Intro to Elec and Compu Engrng	LAB	L06	4	W	9:30 AM	12:20 PM	LGT ENGR LAB	283	David Westerfeld
ESE	123	Intro to Elec and Compu Engrng	LAB	L07	4	F	9:30 AM	12:20 PM	LGT ENGR LAB	283	David Westerfeld
ESE	124	Programming Fundamentals	LEC	1		MW	9:30 AM	10:50 AM	STALLER CTR	3220	Jenny Chen
ESE	124	Programming Fundamentals	LAB	L01	4	T	9:30 AM	12:20 PM	LGT ENGR LAB	281	Jenny Chen
ESE	124	Programming Fundamentals	LAB	L02	4	R	9:30 AM	12:20 PM	LGT ENGR LAB	281	Jenny Chen
ESE	188	Understanding ML	LEC	1	3	MW	2:00 PM	3:20 PM	HUMANITIES	1006	Kurt Butler
ESE	224	Adv. Prog. & Data Structures	LEC	1		TR	2:00 PM	3:20 PM	EARTH&SPACE	131	Xin Wang
ESE	224	Adv. Prog. & Data Structures	LAB	L01	4	M	3:30 PM	6:20 PM	LGT ENGR LAB	281	Xin Wang
ESE	224	Adv. Prog. & Data Structures	LAB	L03	4	F	9:30 AM	12:20 PM	LGT ENGR LAB	281	Xin Wang
ESE	224	Adv. Prog. & Data Structures	LAB	L04	4	F	2:00 PM	4:50 PM	LGT ENGR LAB	281	Xin Wang
ESE	271	Electrical Circuit Analysis	LEC	1	3	MW	6:30 PM	7:50 PM	FREY HALL	102	Sergey Suchalkin
ESE	280	Embedded Systems Design I	LEC	1		TR	3:30 PM	4:50 PM	ENGINEERING	143	Kenneth Short
ESE	280	Embedded Systems Design I	LAB	L01	4	W	8:00 AM	10:50 AM	LGT ENGR LAB	230	Kenneth Short
ESE	280	Embedded Systems Design I	LAB	L02	4	W	2:00 PM	4:50 PM	LGT ENGR LAB	230	Kenneth Short
ESE	280	Embedded Systems Design I	LAB	L03	4	R	8:00 AM	10:50 AM	LGT ENGR LAB	230	Kenneth Short
ESE	280	Embedded Systems Design I	LAB	L04	4	F	8:00 AM	10:50 AM	LGT ENGR LAB	230	Kenneth Short
ESE	280	Embedded Systems Design I	LAB	L05	4	F	2:00 PM	4:50 PM	LGT ENGR LAB	230	Kenneth Short
ESE	301	Engineering Ethics	LEC	1	2	TR	6:30 PM	7:50 PM	MELVILLE LBR	W4550	Donna Tumminello
ESE	305	Deterministic Signals & Systms	LEC	1	3	MW	3:30 PM	4:50 PM	HUMANITIES	1006	Vibha Mane
ESE	319	EM Waves & Transmission Lines	LEC	1	3	TR	12:30 PM	1:50 PM	MELVILLE LBR	W4525	Jayant Parekh
ESE	323	Mod Circuit Board Des & Proto	LEC	1		MW	3:30 PM	4:50 PM	MELVILLE LBR	W4550	David Westerfeld
ESE	323	Mod Circuit Board Des & Proto	LAB	L01	3	HTBA	1:00 AM	1:00 AM	LGT ENGR LAB	283A	David Westerfeld
ESE	323	Mod Circuit Board Des & Proto	LAB	L02	3	HTBA	1:00 AM	1:00 AM	LGT ENGR LAB	283A	David Westerfeld
ESE	323	Mod Circuit Board Des & Proto	LAB	L03	3	HTBA	1:00 AM	1:00 AM	LGT ENGR LAB	283A	David Westerfeld
ESE	325	Modern Sensors	LEC	1	3	TR	11:00 AM	12:20 PM	MELVILLE LBR	N3063	Leon Shterengas); Ridha Kamoua
ESE	331	Semiconductor Devices	LEC	1	3	MW	2:00 PM	3:20 PM	FREY HALL	201	Ridha Kamoua
ESE	333	Real-Time Operating Systems	LEC	1	3	T	6:30 PM	9:20 PM	FREY HALL	305	Yanhang Shi
ESE	342	Communication Systems	LEC	1	3	TR	2:00 PM	3:20 PM	FREY HALL	313	Harbans Dhadwal
ESE	345	Computer Architecture	LEC	1	3	MW	5:00 PM	6:20 PM	LGT ENGR LAB	152	Mikhail Dorojevets
ESE	346	Computer Communications	LEC	1	3	MW	11:00 AM	12:20 PM	FREY HALL	301	Thomas Robertazzi
ESE	352	Electromech Energy Converters	LEC	30	3	W	3:30 PM	6:20 PM	MELVILLE LBR	E4315	Yifan Zhou
ESE	358	Computer Vision	LEC	1	3	TR	11:00 AM	12:20 PM	FREY HALL	217	Murali Subbarao
ESE	388	Foundations of Machine Learn	LEC	1	3	MW	6:30 PM	7:50 PM	FREY HALL	205	Vibha Mane
ESE	440	Senior Design I	LEC	1	3	TR	5:00 PM	6:20 PM	FREY HALL	104	Harbans Dhadwal
ESE	441	Senior Design II	LEC	1		TR	6:30 PM	7:50 PM	LGT ENGR LAB	213	Harbans Dhadwal
ESE	441	Senior Design II	TUT	T07	3	APPT	1:00 AM	1:00 AM	LGT ENGR LAB	213	Harbans Dhadwal
ESE	451	Power Electronics	LEC	1	3	TR	9:30 AM	10:50 AM	FREY HALL	216	Fang Luo